

# EAST Search History

R.A.

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	93	noguchi near2 yukihiro	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:02
S2	3	kaya near2 junka	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:04
S3	120337	(signal near3 path\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:05
S4	75830	(buffer near3 (circuit\$1   element\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:05
S5	1183995	(output near3 (path\$1   signal\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:06
S6	415	S3 same S4 same S5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:06
S7	570099	(switch\$3 near3 (element\$1   circuit\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:07
S8	58	S6 same S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:09
S9	2562	(345/98,100).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:07
S10	0	S8 & S9	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:07
S11	104136	(shift near2 register)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:10

## EAST Search History

S12	12458	((final   last) near4 stage\$1 near4 output\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:11
S13	24503	((first) near4 stage\$1 near4 output\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:11
S14	0	S4 same S7 same S11 same S12 same S13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:12
S15	119	S4 & S7 & S11 & S12 & S13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:13
S16	14	S9 & S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:25
S17	976666	(control\$3 near3 circuit\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:25
S19	93	S4 & S7 & S11 & S12 & S13 & S17	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:40
S20	954	(bi\$1direction\$2 near3 shift near3 register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:40
S21	106948	(out\$1put near4 buffer)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:42
S22	0	S7 same S17 same S20 same S21	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:43
S23	60	S7 & S17 & S20 & S21	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/12 17:51
S24	954	(bi\$1direction\$2 near3 shift near3 register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/14 16:11

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S25	977127	(control\$3 near\$3 circuit\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/14 16:13
S26	331524	(out\$1put\$4 near\$4 switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/14 16:23
S27	5	S24 same S25 same S26	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/14 16:18
S28	0	(out\$1put\$4 same switch\$3 same buffer same control\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/14 16:24
S29	27564	(out\$1put\$4 same switch\$3 same buffer same circuit\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/14 16:26
S30	12	S24 same S29	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/14 16:26
S31	6	("4578672"   "5517208"   "5675354"   "5677706"   "5805117").PN. OR ("6731260").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/14 17:11
S32	966	(bi\$1direction\$3 near\$3 shift\$3 near\$3 register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:53
S33	319190	(feedback same (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:52
S34	1646158	(buffer\$1   amplif\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 11:01
S35	5	S32 same S33 same S34	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 11:01
S36	3380617	((signal   path   line) same (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 11:00

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S37	70	S32 same S34 same S36	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 11:05
S38	2141320	(buffer\$1   amplif\$4   invert\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 11:01
S39	13	S32 same S33 same S38	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 11:02
S40	8	S39 not S35	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 11:02
S41	67	S37 not S39	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 11:05
S42	9	("20040239659"   "3051848"   "3584308"   "5170368"   "5719913"   "6418182"   "6427158"   "6724363").PN. OR ("6996203"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:40
S43	344195	inspect\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:40
S44	2	S32 same S43	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:40
S45	71476	(shift near3 register)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:43
S46	5013	S43 & S45	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:41
S47	930	S33 same S45 same S38	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:43
S48	2	S33 same S45 same S38 same S43	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:42
S49	68336	(shift adj register)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:43

## EAST Search History

S50	908	S33 same S49 same S38	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:44
S51	198796	(feedback near4 (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 12:43
S52	262	S51 same S49 same S38	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:44
S53	58421	"345"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:44
S54	8	S52 & S53	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:52
S55	34	S47 & S53	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:57
S56	8215	((first   final   last) near3 stage\$1) same (feedback same (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3)))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:58
S57	13	S32 & S38 & S56	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 12:58
S58	14	("3696393"   "4499459"   "5138310"   "5151689"   "6049321"   "6215533").PN. OR ("6377235").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 13:04
S59	117214	(signal near4 path\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 13:05
S60	93	S32 & S59	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 13:05
S61	39	S32 & S33 & S59	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/20 13:05
S62	737618	((out\$1put near3 signal) same (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:08
S63	130	S32 same S62	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:08

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S64	50	S32 same S38 same S62	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:08
S65	15604	(driver same feedback same (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:18
S66	6100	S38 same S65	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:18
S67	127	S53 & S66	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:19
S68	1605	((signal   data   video) near3 line near3 (driv\$3   circuit) same feed\$1back same (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:24
S69	729	S38 same S68	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:26
S70	7	S32 & S69	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:22
S71	1622	((column   signal   data   video) near3 line near3 (driv\$3   circuit) same feed\$1back same (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:27
S72	736	S38 same S71	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:27
S73	7	S32 & S72	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:27
S74	301	((column   signal   data   video) near3 line near3 (driv\$3   circuit) near4 feed\$1back near5 (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:35

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S75	115	S38 same S74	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:28
S76	2	S53 & S75	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:29
S77	57653	((first near3 output near3 (path   signal   line)) same ((second   last) near3 output near3 (path   signal   line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:31
S78	48	S74 & S77	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:32
S79	293	((scan\$4   row) near3 (driv\$3   circuit) near4 feed\$1back near5 (control\$3   circuit   processor   micro\$1control\$3   micro\$1process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:36
S80	46	S38 same S79	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 13:36
S81	95922	((final   last) near3 stage\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:58
S82	1424	S33 same S81	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:53
S83	0	S32 same S82	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:53
S84	0	S32 & S82	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:53
S85	122128	(shift\$3 near3 register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:53
S86	161	S82 same S85	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:54

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S87	23	S38 same S86	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:56
S88	4	S79 same S81	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:56
S89	78	((final   last) near3 stage\$1) near5 feedback near5 control\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 15:58
S90	37	S38 same S89	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 16:36
S91	34609	(connector near3 pin)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 16:36
S92	0	S32 same S91	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 16:37
S93	9	S32 & S91	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/20 16:37
S94	3618	((bi\$direction\$3   (dual near2 direction\$3)) near8 register\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/10/18 13:38
S95	95530	(buffer\$3 same switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/10/18 13:39
S96	80	S94 same S95	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/10/18 14:19
S97	57245	((inspect\$4   check\$4) near4 circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/10/18 14:20
S98	20	S94 same S97	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/10/18 14:24



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S99	3180	345/98-100.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/10/18 14:26
S10 0	88	S94 & S99	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/10/18 14:27
S10 1	134	(buffer\$3 & switch\$3 & bi\$1direction\$3).clm.	US-PGPUB	OR	OFF	2006/10/18 14:28
S10 2	0	(buffer\$3 & switch\$3 & bi\$1direction\$3 & (final near4 (stage   state))).clm.	US-PGPUB	OR	OFF	2006/10/18 14:29
S10 3	0	(buffer\$3 & switch\$3 & bi\$1direction\$3 & (final near8 (stage   state))).clm.	US-PGPUB	OR	OFF	2006/10/18 14:29
S10 4	0	(buffer\$3 & switch\$3 & bi\$1direction\$3 & (final near8 (stage\$1   state\$1))).clm.	US-PGPUB	OR	OFF	2006/10/18 14:30
S10 5	17	(buffer\$3 & switch\$3 & bi\$1direction\$3 & (check\$3   inspect\$4)).clm.	US-PGPUB	OR	OFF	2006/10/18 14:30
S10 6	2772	345/204.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/10/23 16:25